

# Kahn Process Networks and MPEG decoding

K.A. Vissers<sup>1,2</sup>, Bart Kienhuis<sup>2,3</sup>

Pieter van der Wolf<sup>4</sup>, Mudit Goel<sup>2</sup>,

Paul Lieverse<sup>4,5</sup>, Ed Deprettere<sup>3</sup>

<sup>1</sup> Trimedia Technologies Inc

<sup>2</sup> UC Berkeley

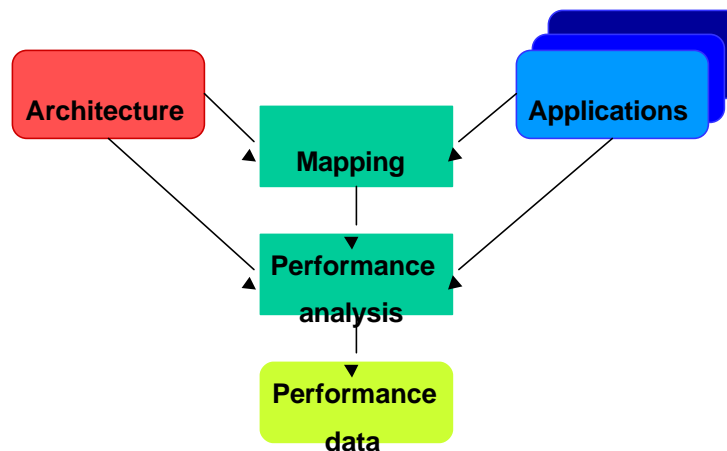
<sup>3</sup> Leiden University

<sup>4</sup> Philips Research

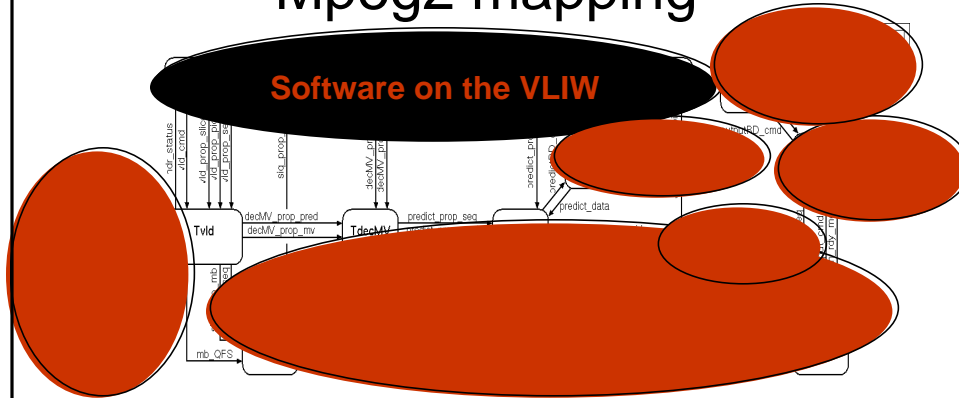
<sup>5</sup> Delft University of technology



## Y-chart for architecture exploration



# Mpeg2 mapping



Dedicated Mpeg coprocessor: pipe with fifo buffers and blocking semantics

# Design Space Exploration

