Homework 5

EE 290n - Advanced Topics in Systems Theory

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- 1. This problem considers parallel and concurrent scheduling of the synchronous dataflow model in figure 1. Assume that actors *a* and *c* have execution times of 1 and actor *b* has an execution time of 4.
 - (a) Write the balance equations for this model in matrix form.
 - (b) Find the least positive integer solution to the balance equations.
 - (c) An **acyclic precedence graph** (**APG**) is a graph that shows the dependencies of actor firings. Specifically, it contains one node for each firing of an actor, and a directed arc from one firing to the next if the first firing must occur before the second. Give the APG for the firings represented by your solution in (b). You should assume that each firing of an actor depends on previous firings of the actor (so that the actor can have state).
 - (d) The latency between actor a and actor c is defined as the maximum time between the start of a firing of a and the start of a firing of actor c that depends directly on that firing of a. Find a single processor, non-preemptive schedule that minimizes this latency. What is the latency?
 - (e) Suppose that actors *a* and *c* represent real-time interactions via sensors and actuators and that they must execute at regular time intervals. That is, actor *a* must fire every τ time units, like clockwork. What is the minimum value for τ given the schedule in the previous part? Given this value, what is the processor utilization?
 - (f) Consider a single-processor schedule that allows preemption. Is a smaller value for τ achievable? What is the minimum? What is the processor utilization? Assume the overhead associated with a preempting an execution is zero.
 - (g) The **makespan** of a set of firings is the time it takes to complete the entire set. Give a two-processor schedule that minimizes the makespan for the set of firings in your solution to part (b). If you assume no overlap between executions of successive iterations, what is the processor utilization?



Figure 1: A synchronous dataflow model with production and consumption rates shown.

- (h) Find the optimal *N*-processor schedule that allows overlapped execution of successive iterations (pipelining). What is *N*? What is the time τ between firings of *a*? What is the processor utilization? Assume the cost of interprocessor communication is zero.
- 2. This problem explores the use of the SDF model of computation together with modal models to improve expressiveness. In particular, you are to implement a simple run-length coder using no director other than SDF, leveraging modal models with state refinements. Specifically, given an input sequence, such as

you are to display a sequence of pairs (i, n), where *i* is a number from the input sequence and *n* is the number of times that number is repeated consecutively. For the above sequence, your output should be

Note that this pattern arises commonly in many coding applications, including image and video coding.